



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Natsuki KUSHIYAMA

Serial No: 09/887,875

Filed: June 25, 2001

For: Semiconductor Integrated Circuit

and Semiconductor Apparatus

System

Art Unit: 2181

Examiner: Not Assigned

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to:

Commissioner for Patents Washington D.C. 20231, on

October 2, 2001
Date of Deposit
Shindale Ferguson

- Leguson October 2, 2001

PETITION FOR CORRECTED FILING DATE

Deputy Assistant Commissioner for Patents Washington, D.C. 20231

Dear Sir:

Applicant respectfully submits a request to the Deputy Assistant Commissioner for Patents for a Corrected Filing Date of June 22, 2001. The above-referenced patent application was mailed via express mail to the United States Patent and Trademark Office on June 22, 2001, as shown in the enclosed copy of the sales receipt for express mail delivery of the envelope enclosing the patent application.

Additionally, a statement by a representative of the Postal Service has been issued, confirming that the mailing date of the application was June 22, 2001. Due to an omission of the mailing date by the postal clerk, the U.S.P.T.O. issued the filing date of the above-reference application as the date the application was received, June 25, 2001. A check in the amount of \$130.00 is enclosed.

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Please charge any insufficiency or credit any overpayment to Deposit Account No. 50-1314. A copy of the petition is enclosed.

Respectfully submitted,

HOGAN & HARTSON L.L.P.

Date: October 2, 2001

Brian Martin

Registration No. 47,771 Attorney for Applicant(s)

500 South Grand Avenue, Suite 1900

Los Angeles, California 90071

Phone: 213-337-6700 Fax: 213-337-6701

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SEE REVERSE SIDE FOR SERVICE GUARANTEE AND LIMITS ON INSURANCE COVERAGE

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THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Natsuki KUSHIYAMA

Serial No: 09/887,875

Filed: June 25, 2001

Semiconductor Integrated Circuit and For:

Semiconductor Apparatus System

ASSOCIATE POWER OF ATTORNEY (37 C.F.R. § 1.34)

Commissioner for Patents Washington, D.C. 20231

Dear Sir:

Examiner: Not Assigned

2181

Art Unit:

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Commissioner for Patents Washington D.C. 20231, on

October 2, 2001 Date of Deposit Shifidale Ferguson

October 2, 2001 Date

Please recognize the following as my associate attorneys in the above-entitled application:

STUART LUBITZ, Reg. No. 20,680; JOHN P. SCHERLACHER, Reg. No. 23,009; ALFRED A. D'ANDREA, JR., Reg. No. 27,752; WILLIAM J. KUBIDA, Reg. No. 29,664; MATTHEW BAILEY, Reg. No. 33,829; STUART T. LANGLEY, Reg. No. 33,940; E. MICHAEL BYORICK, Reg. No. 34,131; CAROL W. BURTON, Reg. No. 35,465; STEVEN C. PETERSON, Reg. No. 36,238; WILLIAM H. WRIGHT, Reg. No. 36,312; STEVEN K. BARTON, Reg. No. 36,445; MICHAEL L. CRAPENHOFT, Reg. No. 37,115; DAVID LUBITZ, Reg. No. 38,229; WEI-NING YANG, Reg. No. 38,690; CELINE CROWSON, Reg. No. 40,357; STERLON R. MASON, Reg. No. 41,179; SARAH S. O'ROURKE, Reg. No. 41,226; LAWRENCE J. McCLURE, Reg. No. 44,228; MATTHEW G. DYOR, Reg. No. 45,278; WEI-FU HSU, Reg. No. 45,723; ERIN P. MADILL, Reg. No. 46,893; BRIAN D. MARTIN, Reg. No. 47,771; KENTON B. ABEL, Reg. No. P49,05.

Please associate this case with this firm's customer number:

☐ Customer Number | 000026021

PATENT TRADEMARK OFFICE

Respectfully submitted,

HOGAN & HARTSON L.L.P.

Date: October 2, 2001

500 South Grand Avenue, Suite 1900

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Phone: 213-337-6700 Fax: 213-337-6701

Wei-Ning Yang

Registration No. 38,690

Attorney for Applicant(s)

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N THE UNITED STATES PATENT AND TRADEMARK OFFICE

lication of:

Natsuki KUSHIYAMA

Serial No: 09/887,875

Filed: June 25, 2001

Semiconductor Integrated Circuit

and Semiconductor Apparatus

System

Group Art Unit: 2181

Examiner:

Not Assigned

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Commissioner for Patents Washington D.C. 20231, on

October 2, 2001 Date of Deposit

Shindale/Ferguson

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents Washington, D.C. 20231

Dear Sir:

Request is hereby made for correction of the filing receipt (copy enclosed) for the above-identified application. Please note that the Filing Date, "June 22, 2001" has been incorrectly entered as, "June 25, 2001" as discussed in the attached copy of the Petition For Corrected Filing Date.

No fee is required for this request. However if a fee should be required, please charge it to Deposit Account No. 50-1314 of the undersigned attorney's firm. A copy of this Request is enclosed.

Respectfully submitted,

HOGAN & HARTSON L.L.P.

Date: October 2, 2001

Brian Martin

Registration No. 47,771 Attorney for Applicant(s)

500 South Grand Avenue, Suite 1900

Los Angeles, California 90071

Telephone: 213-337-6700 Facsimile: 213-337-6701

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COMMISSIONER FOR PATENTS United States Patent and Trademark Office WASHINGTON, D.C. 20231

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APPLICATION NUMBER

GRP ART UNIT FILING DATE

2181

FIL FEE REC'D ATTY.DOCKET.NO

DRAWINGS

TOT CLAIMS

IND CLAIMS

09/887,875

-06/25/2001

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81790.0204

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06/22/2001

CONFIRMATION NO. 8040

FILING RECEIPT

OC000000008433703°

000026021 **HOGAN & HARTSON L.L.P.** 500 S. GRAND AVENUE **SUITE 1900** LOS ANGELES, CA 90071-2611

Date Mailed: 08/16/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an err r is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Natsuki Kushiyama, Kawasaki-shi, JAPAN;

Assignment For Published Patent Application

KABUSHIKI KAISHA TOSHIBA:

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Domestic Priority data as claimed by applicant

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Foreign Applications

JAPAN 2000-188857 06/23/2000

If Required, Foreign Filing License Granted 08/16/2001

Projected Publication Date: 12/27/2001

N n-Publication Request: No

Early Publication Request: No

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Title

Semiconductor integrated circuit and semiconductor apparatus system

Preliminary Class

Data entry by : SARWARI, HOMA

Team : OIPE

Dat: 08/16/2001

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LICENSE FOR FOREIGN FILING UNDER Title 35, United States C d , Secti n 184 Title 37, C d f Federal R gulations, 5.11 & 5.15

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| REQUEST FOR PATENT FEE REFUND | | | | | | | |
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| 1 Date of Request: 1/-1-01 2 Serial/Patent # 09/887,475 | | | | | | | |
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